

**MP1764A  
Error Detector  
(GPIB Programming)  
Option 02 Differential Data Input Function  
Option 03 Clock Recovery Function  
Operation Manual**

**First Edition**

- **Read this manual before using the equipment.**
- **To ensure that the equipment is used safely, read the "For Safety" in the MP1764A Error Detector Operation Manual first.**
- **Keep this manual with the equipment.**
- **The MP1764A-02 and MP1764A-03 do not conform to CE Marking.**

**ANRITSU CORPORATION**

MP1764A

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Operation Manual

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## MP1764A-02 Differential Data Input and MP1764A-03 Clock Recovery Function

For an operation explanation of the MP1764A-02 Differential Data Input Function (hereafter, referred to as Option 02) and MP1764A-03 Clock Recovery Function (hereafter, referred to as Option 03), refer to part of “MP1764D Error Detector (GPIB Programming) Operation Manual.” The following lists show the referred pages.

In addition, the MP1764D Error Detector (GPIB Programming) Operation Manual is included in this manual. Read replacing the model name with the MP1764A.

As for operations of the MP1764A mainframe, refer to “MP1764A Error Detector (GPIB Programming) Operation Manual” (Document No. W0888AE).

When option 02 is installed.

### Valid device messages

Function	Header Part of Control message	Details page	Related Page
Input data switching	DSL	9-151	9-8, 9-10 to 9-11, A-6, C-1
Tracking/Adjust operation	DAC	9-152	
Input signals setting	DSD	9-153	
Termination differential selection	DTC	9-158	

When option 03 is installed.

### Valid device messages

Function	Header Part of Control message	Details page	Related Page
Supplied clock selection	RRC	9-154	9-8, 9-10 to 9-11, A-6, C-1
PLL reset/PLL status	RPL	9-155	
Recovery frequency setting	RFQ	9-156	

\* The valid commands become invalid if the corresponding option is not installed.

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